



PATENT
TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of :
Khandros et al :
Application No.: 08/030,194 : Group Art Unit: 2104
Filed: April 28, 1993 : Examiner: D. Russele
For: SEMICONDUCTOR CHIP ASSEMBLIES, : Date: July 18, 1994
METHODS OF MAKING SAME AND :
COMPONENTS FOR SAME :
X

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231


INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Respectfully submitted,

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(Date)


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Marcus J. Millet

Typed or Printed Name of Person Signing Certificate



PATENT
TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
KHANDROS ET AL.

Serial No.: 08/030,194

Filed: April 28, 1993

For: SEMICONDUCTOR CHIP
ASSEMBLIES, METHODS OF
MAKING SAME AND COMPONENTS:
FOR SAME

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: Group Art Unit: 2104
: Examiner: D. Russele
: Date: February 27, 1995

:
X

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

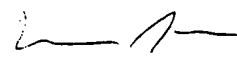
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Khandros et al

Serial No.: 08/030,194

Filed: April 28, 1993

For: **Semiconductor Chip Assemblies,
Methods of Making Same And
Components For Same**



Group Art Unit: 2503

Examiner: S. Clark

Date: March 1, 1996

Assistant Commissioner For Patents
Washington, D.C. 20231

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TESSERA 3.3-018



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of
Khandros et al

Serial No.: 08/030,194

Filed: April 28, 1993

For: **Semiconductor Chip Assemblies,
Methods of Making Same And
Components For Same**

Group Art Unit: 2503

Examiner: S. Clark

Date: October 18, 1996

#18

Assistant Commissioner For Patents
Washington, D.C. 20231


INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. A copy of each reference is enclosed herewith. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material. Comments concerning the relevance of certain references are included in the accompanying amendment and response.

Respectfully submitted,

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